

FIG. 1

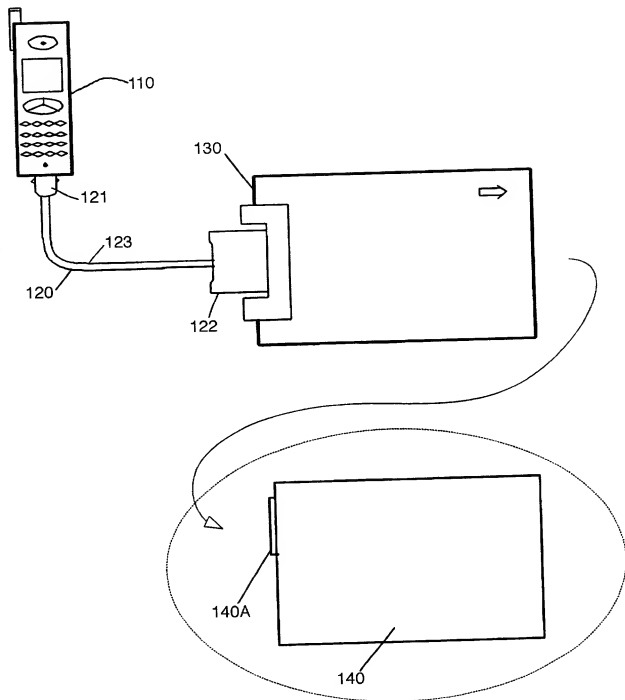


FIG. 2

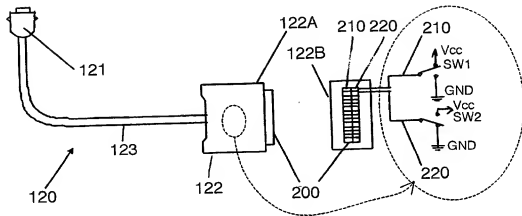
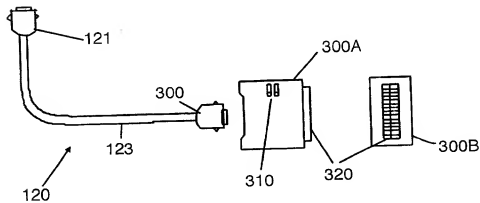
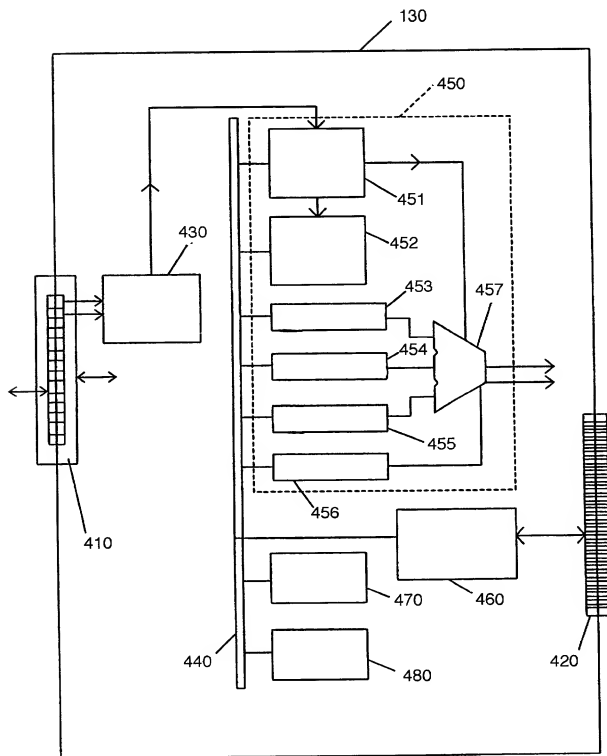


FIG. 3



10045095-011502

FIG. 4



10045005-011500

FIG. 5

Communication System	PDC	PHS Guarantee	PHS Best Effort
Communication Protocol	WORM-ARQ	PIAFS 2.0	PIAFS 2.1
Hardware Interface Specification	Frame Signal Synchronization Half-Duplex Transmission External Clock	Frame Signal Synchronization Full-Duplex Transmission External Clock	Frame Signal Synchronization (switching to 4bit/8bit) Full-Duplex Transmission External Clock
Dedicated Connector	16 Pins	18 Pins	12 Pins

COMMUNICATION SYSTEMS EMPLOYED BY A CELLULAR PHONE

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